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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**In re the Application of  
**Alviar**Docket Number: **TI-36227**Serial No.: **10/706,389**Art Unit: **1733**Filed: **November 10, 2003**Examiner: **J. Brown**For: **METHOD AND SYSTEM FOR INTEGRATED CIRCUIT BONDING****CERTIFICATION OF FACSIMILE TRANSMISSION**

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NAME OF INVENTOR(S): <b>ALVIAR</b>		RECEIPT DATE & SERIAL NO.:
TITLE OF INVENTION: <b>METHOD AND SYSTEM FOR INTEGRATED CIRCUIT BONDING</b>		Serial No.: <b>10/706,389</b> Conf. No: <b>6375</b>
TI FILE NO.:	DEPOSIT ACCT. NO.:	
<b>TI-36227</b>	<b>20-0668</b>	
FAXED: <b>11/30/2005</b> DUE: <b>11/30/2005</b> ATTY/SEC'Y: <b>YT/jsm</b>		

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Texas Instruments Incorporated  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION**

Appl. No. : 10/706,389  
 Applicant : Melvin Bringas Alviar  
 Filed : November 10, 2003  
 TC/A.U. : 1733  
 Examiner : Brown, Jayme L  
 Docket No. : TI-36227  
 Customer No. : 23494

Confirmation No. 6375

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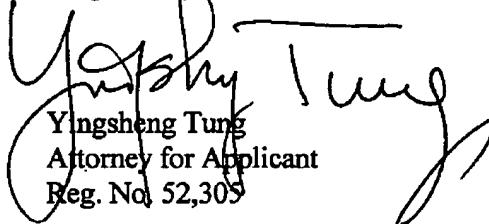
**ELECTION**

Sir:

In response to the Examiner's restriction requirement mailed October 31, 2005, applicant elects Group I, claims 1-12 for prosecution purposes.

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Respectfully submitted,

  
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